UNIVERSITY OF SWAZILAND FINAL EXAMINATION 2006

Title of paper: INTRODUCTION TO LOGIC

Course number: CS235

Time allowed: Three (3) hours

Instructions: Answer any five (5) of the seven (7) questions.

This examination paper should not be opened until permission has been granted by the invigilator.

a) Write the dual of the following logical equivalence:

b) Prove by truth table the validity of the contra-positive law of logical equivalence.

[4]

c) Prove by truth table that the following propositions are consistent:

[10]

- $P \vee Q$
- $\neg R \Rightarrow P$
- $Q \Rightarrow \neg P$
- d) Prove by perfect induction that the following conclusion is entailed by the three premises given in part c) above.

[4]

 $\neg (P \Leftrightarrow Q)$

Question 2

a) Prove the following using the laws of logical equivalence:

[11]

$$P \wedge Q \Rightarrow (R \Leftrightarrow \neg P) \equiv \neg (P \wedge Q \wedge R)$$

b) Simplify the following proposition using the laws of logical equivalence:

[9]

Prove, by natural deduction, the validity of conclusions a) and b) based on the following premises:

- P ∨ Q ⇒ R
 S ⇒ Q ∧ R

- a) $P \Rightarrow R$

b) $Q \Leftrightarrow S$

[7]

[13]

Question 4

a) Define the function f (a,b,c) in conjunctive normal form:

[8]

а	b	C	f(a,b,c)
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	0

b) Implement a circuit for the function g (a,b,c), using NOR gates alone:

[6]

$$g(a,b,c) = \overline{ab+c}$$

c) Write the following numbers in 9-bit binary according to the twos-complement system. Show all steps in your working.

[6]

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a) Minimize the function f(a,b,c,d) using a Karnaugh map:

[9]

$$f(a,b,c,d) = abcd + \overline{abc}.\overline{d} + \overline{a}.\overline{b}.\overline{d}$$

Assume that the following inputs are impossible:

b) Minimize the function g (a,b,c,d) using the Quine-McCluskey method:

$$g(a,b,c,d) =$$

$$abcd + a\overline{b}cd + abcd + abcd + a\overline{b}cd + a\overline{b}cd + abcd$$

Question 6

a) Draw the action tables of the RS latch and the JK flip flip.

[4]

b) Draw a circuit diagram showing how the JK flip flop is constructed from RS latches.

[8]

c) Draw a circuit that inputs a 3-bit number, and then performs modulo addition of 5 to it, outputting the 3-bit sum (see table below). The circuit should contain a number of adders.

[8]

input	output
0	5
1	6
2	7
3	0
4	1
5	2
6	3
7	4

a) Copy the following predicate and circle each occurrence of a free variable:

$$\neg\exists x \big(P(x, y) \Rightarrow Q(x) \land \neg \forall y \big(Q(y) \big) \big)$$

[3]

b) Rewrite the predicate in part a) above such that all variables are:

[8]

- i. Universally quantified.
- ii. Existentially quantified.
- c) Give a model of the first predicate that is also a model of the second predicate:

[9]

- $\forall x (\forall y (P(x, y) \Rightarrow Q(x, y)))$